

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No.10/757,276
Filing DateJanuary 14, 2004
Inventor.....Kevin Beaman, et al.
Assignee.....Micron Technology, Inc.
Group Art Unit.....2813
ExaminerStephen Smoot
Attorney's Docket No.MI22-2475
Customer No.....021567
Title: A Method of Forming a Structure over Semiconductor Substrate

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Reference - - See attached Form PTO-1449

The attached Form PTO-1449 is submitted In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the references listed on the attached Form PTO-1449. Pursuant to Federal Register Vol. 69, No. 182, pg. 56542 (September 21, 2004), no copies of any cited U.S. patents or U.S. published applications are included herewith. Copies of all other cited art references, if any, are attached. No admission is made regarding whether the submitted references are prior art.

Citation of this reference is respectfully requested.

Respectfully submitted,

Date: 9-18-07



D. Brent Kenady
Reg. No. 40,045
Customer No. 021567

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. MI22-2475	SERIAL NO. 10/757,276	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Kevin L. Beaman		
				FILING DATE January 14, 2004	GROUP 2813	
U.S. PATENT DOCUMENTS						
Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA					
	AB					
	AC					
	AD					
	AE					
	AF					
	AG					
	AH					
	AI					
	AJ					
	AK					
	AL					
	AM					
	AN					
	AO					
	AP					
	AQ					
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)						
	AR		Millman, Microelectronics, 1979, p. 91-92			
	AS					
EXAMINER			DATE CONSIDERED			
<p>*EXAMINER: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>						